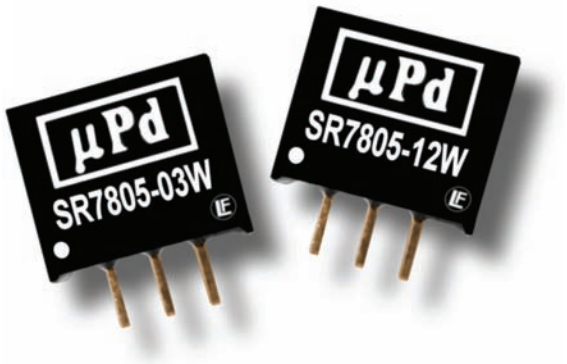
SR7805 Series

Low Cost, Non-isolated Ultra-Miniature SIP POL Switching Regulators



Key Features:

- Efficiency to 96%
- Ultra-Miniature SIP Case
- LM78xx Replacement
- Wide Input Range
- Short Circuit Protected
- Thermal Shutdown
- Low Noise

RoHS Compliant

Electrical Specifications

Specifications typical @ +25°C, nominal input voltage & rated output current, unless otherwise noted. Specifications subject to change without notice.

Output

Parameter	Conditions	Min.	Typ.	Max.	Units
Output Voltage Accuracy			±2.0	±3.0	%
Line Regulation	Vin = Min to Max		±0.2	±0.4	%
Load Regulation	Iout = 10% to 100%		±0.4	±0.6	%
Ripple & Noise (20 MHz)			20	35	mV P - P
Dynamic Load Stability	Between 10% - 100% Load			±100	mV
Output Power Protection		120			%
Thermal Shutdown	See Note 1		150		°C
Quiescent Current	See Note 2		5	7	mA
Output Current Limit				2,000	mA
Output Short Circuit	Continuous (Autorecovery)				

General

Parameter	Conditions	Min.	Typ.	Max.	Units
Isolation Voltage	Not Isolated				
Switching Frequency		280	330	450	kHz

Environmental

Parameter	Conditions	Min.	Typ.	Max.	Units
Operating Temperature Range	Ambient	-40	+25	+85	°C
Operating Temperature Range	Case			+100	°C
Storage Temperature Range		-55		+125	°C
Lead Temperature	1.5 mm From Case For 10 Sec			300	°C
Cooling	Free Air Convection				
Humidity	RH, Non-condensing			95	%

Physical

Case Size	0.45 x 0.3 x 0.40 Inches (11.5 x 7.55 x 10.2 mm)
Case Material	Non-Conductive Black Plastic (UL-94V0)
Weight	0.07 Oz (2.0g)

Reliability Specifications

Parameter	Conditions	Min.	Typ.	Max.	Units
MTBF	MIL HDBK 217F, 25°C, Gnd Benign	2.0			MHours

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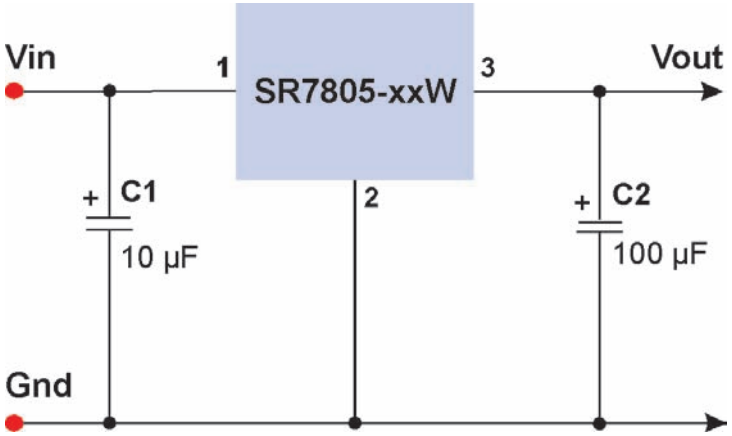
Model Selection Guide

Model Number	Input Voltage Range (VDC)	Output		Efficiency (%, Typ)	
		Voltage (VDC)	Current (mA, Max)	Min Vin	Max Vin
SR7805-03W	4.75 - 28.0	3.3	500.0	90	80
SR7805-05W	6.5 - 32.0	5.0	500.0	93	84
SR7805-06W	8.0 - 32.0	6.5	500.0	94	87
SR7805-09W	11.0 - 32.0	9.0	500.0	95	91
SR7805-12W	15.0 - 32.0	12.0	500.0	95	92
SR7805-15W	18.0 - 32.0	15.0	500.0	96	93

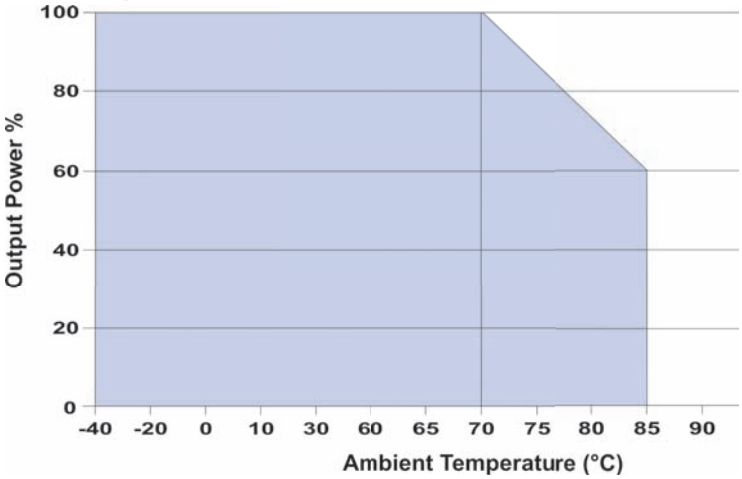
Notes:
1. Dynamic load stability is specified for output loads from 10% to 100%.
2. Quiescent current is specified at 0% load for Vin = min to max.
3. The input should not exceed the range given in the model selection chart above. Exceeding this limit could damage the unit.

Typical Application Circuit

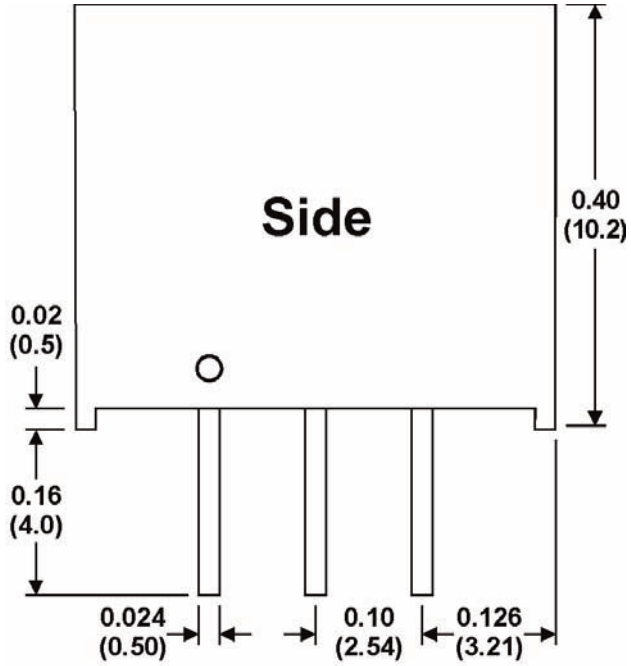


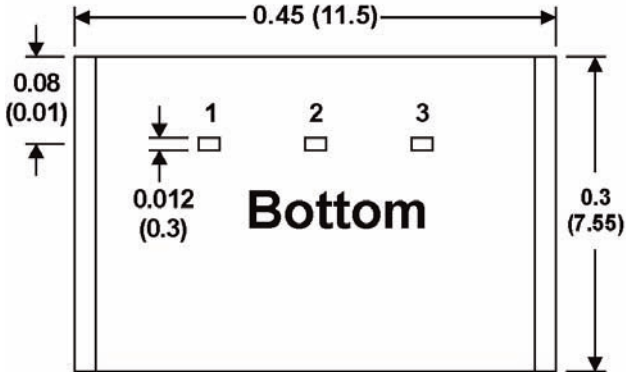
Notes:
1. C1 is a low ESR ceramic capacitor used to minimize noise at the regulator. A tantalum or low ESR electrolytic capacitor may also be used. A typical value is 10 µF/50V.
2. C2 is optional. A typical value is a 100 µF/25V electrolytic capacitor.

Derating Curve



Mechanical Dimensions





Pin Connections

Pin	Description
1	-Vin
2	Gnd
3	+Vout

Notes:
- All dimensions are typical in inches (mm)
- Tolerance x.xx = ±0.01 (±0.25)
- Pin 1 is marked by a "dot" or indentation on the side of the unit

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