

Digi Connect ME® 9210 Specs

Hardware

- 32-bit Digi NS9210 processor @ 75 MHz (ARM926EJ-S)
- On-chip 256-bit AES accelerator
- Flexible Interface Modules (FIM)
 - 300 MHz DRP165X CPU
 - 2k program/192 bytes data RAM
- On-board memory
 - 2/4 MB NOR flash
 - 8 MB SDRAM
- High-speed TTL serial interface
 - Full signal support for TXD, RXD, RTS, CTS, DTR, DSR and DCD
 - Hardware/Software flow control
- Serial Peripheral Interface (SPI)
 - Master data rate up to 16.7 Mbps
 - Slave data rate up to 7.5 Mbps
- I²C v1.0 bus interface
 - 7-bit and 10-bit address modes
- 10 shared GPIO ports with
 - Up to 3 external IRQ options
- Power management modes
 - On-the-fly clock scaling
 - Low power sleep modes
 - Configurable scaling/wake-up events (EIRQ, UART, Ethernet, etc.)
- Software watchdog
- On-board power supervisor
- Wave-solderable design
 - No clean flux process

Network Interface

- Physical layer: 10/100Base-T
- Data rate: 10/100 Mbps (auto-sensing)
- Mode: Full or half duplex (auto-sensing)
- Connector: RJ-45 w/magnetics
- 802.3af power pass-through
 - Mid- and end-span

Environmental

- Operating temperature:
 - 40° C to +80° C (-40° F to +176° F)
- Storage temperature:
 - 50° C to +125° C (-58° F to +257° F)
- Relative humidity:
 - 5% to 90% (non-condensing)
- Altitude: 12,000 feet (3,658 meters)

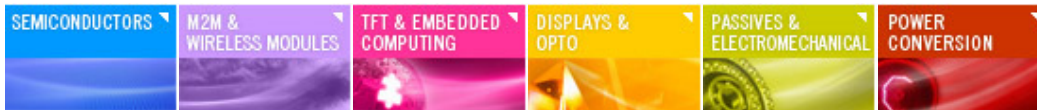
Power Requirements

- 3.3VDC @ 346 mA typical (1.14 W)
- UART and Ethernet activated
Low Speed Idle mode (approx.)
- 3.3VDC @ 186 mA (613 mW)
- /16 clock scaling, Ethernet activated
Sleep Mode (approx.)
- 3.3VDC @ 34 mA (113 mW)
- Wake-up on EIRQ, Ethernet PHY off



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Regulatory Approvals

- FCC Part 15 Class B, EN 55022 Class B
- EN 61000-3-2 and EN 61000-3-3
- ICES-003 Class B, VCCI Class II, AS 3548
- FCC Part 15 Sub C Section 15.247
- IC RSS-210 Issue 5 Section 6.2.2(o)
- EN 300 328, EN 301 489-17
- UL 60950-1, EN 60950 (EU)
- CSA C22.2, No. 60950
- EN 55024

Pinouts

- The table below provides a complete overview of the pinout and available configuration options:

Pin	UART	GPIO	Ext IRQ	I2C	SPI	FIM	Other
1							VETH+
2							VETH-
3-6	Positions removed						
7	RxD	GPIO[3]			IN	PIC[3]	
8	TxD	GPIO[7]			OUT		Timer Out 7 Timer In 8
9	RTS	GPIO[5]	3		CLK		Timer Out 6
10	DTR	GPIO[6]					Timer In 7
11	CTS	GPIO[1]	0			PIC[1]	
12	DSR	GPIO[2]	1			PIC[2]	
13	DCD	GPIO[0]			EN	PIC[0]	
14							/RST
15							3.3V
16							GND
17		GPIO[12]		SDA	CLK		RESET_DONE
18		GPIO[9]	0	SCL			
19	Reserved						
20	GPIO[13]			CLK			INIT Timer Out 9

Dimensions

- Length: 1.445 in (36.7 mm)
- Width: 0.75 in (19.05 mm)
- Height: 0.735 in (18.67 mm)